

Docket No. 210067US2

IN RE APPLICATION OF: Hidemasa ZAMA, et al.

SERIAL NO: 09/883,959

FILED: June 20, 2001

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT, LOGIC OPERATION CIRCUIT, AND FLOW

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Transmitted herewith is an amendment after final in the above-identified application.

- ☐ No additional fee is required
- ☐ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
- ☒ Additional documents filed herewith: Marked-up Copy; Request for Extension of Time (1 month); Information Disclosure Statement under 37 CFR 1.97; Form PTO-1449; European Search Report; Cited References (6)

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS	
TOTAL	16	MINUS	20	0	x \$18 =	\$0.00	
INDEPENDENT	8	MINUS	8	0	x \$84 =	\$0.00	
		<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS			+ \$280 =	\$0.00	
		TOTAL OF ABOVE CALCULATIONS					\$0.00
		<input type="checkbox"/> Reduction by 50% for filing by Small Entity					\$0.00
		<input type="checkbox"/> Recordation of Assignment				+ \$40 =	\$0.00
		TOTAL					\$0.00

- ☒ A check in the amount of **\$290.00** is attached.
- ☒ Please charge any additional Fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- ☒ If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.



22850

Customer Number 22850
Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 10/01)
I:\atty\Wtb\210067US\final-ame-cvr.doc

W. Todd Baker

Gregory J. Maier

Registration No. 25,599

W. Todd Baker

Registration No. 45,265

"RESPONSE UNDER 37 CFR 1.116-
EXPEDITED PROCEDURE EXAMINING
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